

## Electronic Patent Application Fee Transmittal

Application Number:	10709041								
Filing Date:	08-Apr-2004								
Title of Invention:		LOW TRIGGER VOLTAGE ESD NMOSFET TRIPLE-WELL CMOS DEVICES							
First Named Inventor/Applicant Name:		Kiran V. Chatty							
Filer:		Myron Keith Wyche							
Attorney Docket Number:		BUR920030188US1							
Filed as Large Entity									
<b>Utility Filing Fees</b> <table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th style="width: 30%;">Description</th> <th style="width: 15%;">Fee Code</th> <th style="width: 15%;">Quantity</th> <th style="width: 15%;">Amount</th> <th style="width: 15%;">Sub-Total in USD(\$)</th> </tr> </thead> </table>					Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)					
Basic Filing:									
Pages:									
Claims:									
Miscellaneous-Filing:									
Petition:									
Patent-Appeals-and-Interference:									
Post-Allowance-and-Post-Issuance:									
Extension-of-Time:									
Extension - 3 months with \$0 paid		1253	1	1050					
				1050					

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Miscellaneous:</b>				
Request for continued examination	1801	1	810	810
<b>Total in USD (\$)</b>				<b>1860</b>